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Proposal for the EIC Detector R&D program

# Precision Timing Silicon Detectors for a Combined PID and Tracking System at EIC

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## Abstract

We propose an R&D program to establish the applicability of ultra fast silicon sensor technology for constructing a combined time-of-flight (TOF) PID and tracking system at EIC detectors. The low gain avalanche diodes (LGAD) silicon sensors offer an excellent time resolution, and are being used by CMS and ATLAS experiments to build a new high precision timing layer at the high-luminosity (HL) LHC. Proponents have a strong involvement in the R&D and construction of the CMS MIP timing detector (MTD), particularly playing an instrumental role in pushing new PID capabilities with the MTD in the heavy-ion physics program. By leveraging experience at CMS, we propose an R&D program to optimize the timing and position resolution of LGADs sensors to meet the requirements of a compact TOF-tracker system at the EIC. We first propose to systematically study the performance of much thinner LGADs to identify optimal sensor thickness and gain schemes that provide best time resolution (20 ps or better) for PID in a relatively low radiation environment like the EIC. This includes an effort of fabricating thin LGADs at BNL in collaboration with the eRD24 team. Furthermore, we will perform detailed simulations to determine the position resolution of LGADs required for a tracking system, and investigate the feasibility of utilizing ultra-thin trench-isolated (TI) LGADs and AC-coupled LGADs to realize the proposed TOF-tracker system.

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# 1 Responses to the Proposal Evaluation

We thank the committee for their valuable feedback and recommendations, which we have taken into account in the updated proposal. In this section, we summarize our responses to each of the committee's recommendations.

## 1.1 Evaluation

*The proposal leverages experience and technology developed as part of the CMS Phase-2 upgrade project. There are three parts to the proposal. The first part focused on the R&D of ultra-thin LGAD sensors. The sensors will be obtained through existing collaborations. Timing resolutions of 15-20 ps are targeted, possible through test beam measurements at Fermilab. The second part addresses the simulation of a combined TOF-Tracker system. The last element of the one-year proposal is the study of LGAD devices with AC-coupling and trench isolation. AC-LGAD devices obtained from both FBK and BNL will be studied and TI-LGADs from FBK are in hand. Although this project aims at a combined TOF and tracking system, we note the technical complementarity with the already approved EIC R&D effort to apply LGADs in far forward Roman Pot stations (eRD24). We also note that there is technical overlap, particularly in the area of position resolution, AC-LGADs, and system issues. This is a modest proposal and as stated, leverages considerable advantages from the CMS experience. A broad and strong team is in place to carry out the work.*

## 1.2 Recommendations and Our Responses

*Submission of a full proposal is strongly welcomed. The proponents are kindly asked to provide more information in the full proposal on the following aspects:*

- *Elaborate on the reach in particle identification for the proposed tracker-TOF system and the complementarity with the current proposed PID detectors.*

The reach in PID as a function of  $p_T$  and  $\eta$  with the proposed tracker-TOF is shown as shaded areas in Fig. 1, overlaid with particle distributions from PYTHIA e (18 GeV) + p (275 GeV) collisions. Scenarios of one- and three-layer systems are displayed, covering nearly full phase space in the low to intermediate momentum range. The parametrized performance is based on a schematic design shown in Fig. 2.

We have elaborated more on the complementarity of LGADs with not only other proposed PID detectors but also tracking detectors in Sect. 2.2, with a summary in Table 1.

- *Expand on how the goal of 10–15 ps timing resolution will be obtained using MIP particles with real electronics.*

We would like to clarify that our target time resolution for LGADs is about 20 ps or a little better if R&D on thin LGADs comes out to be very successful. We quote a value of 10–15 ps for time resolution of the reference trigger in the test with source or beam, as measured by a MCP-PMT. This is the typical specification of a MCP-PMT available from, e.g. HPK or PHOTEK.

We acknowledge the importance and challenge of realizing front-end electronics with fast timing performance. Although it is unrealistic for us to directly address it in this one-year program, we outline in Sect. 4.4 the prospects of achieving the goal in the longer run. A time resolution of about 20 ps is about within the range of what can be handled by front-end electronics that are developed at CMS and ATLAS. With further R&D and optimization specific for the EIC, it is realistic to achieve the desired performance for actual readout electronics. The bigger challenge in electronics lies in the potential needs for much finer pixelization, which significantly limits the space available for fitting all circuit components in each ASIC channel. This requires extended efforts. More details of our plans in close collaboration with eRD24 are elaborated in Sect. 4.4.

- *Discuss the technical requirements needed for the test beam setup to reach a timing resolution of 10 to 15 ps as well as the feasibility of satisfying those requirements.*

As before, we would like to emphasize that our target time resolution for LGADs is about 20 ps, while 10–15 ps is quoted for the reference trigger in the test measured by a MCP-PMT. In terms of test beam setup, this group had extensive experience with tests carried out at FNAL for the CMS ETL R&D project. For example, Ref. [1] presents results of a beam test at FNAL that Prof. C. Royon’s group at KU has participated in, with more details about the setup in that same reference. Compared to the test stand with sources in Fig. 8, pixel tracking telescope detectors are added along the beam direction to track the trajectory of high-energy protons. In summary, everything required for beam tests of LGADs performance is in place and accessible to the group.

- *If not too early, address the mechanical aspects of the implementation of ultra-thin LGAD detectors for a tracker application covering multiple square meters of area.*

In terms of mechanical designs, we would like to first remind that our proposal is to apply LGADs to outer layers of a tracker to benefit longest flight distance for PID, not an entire tracker based on the LGADs. The layout of the two-disk CMS ETL shown in Fig. 5 is essentially a proto-design of outer layers of an endcap tracker. Furthermore, both CMS and ATLAS will adopt an all-silicon tracker for the HL-LHC program. Mechanical designs of those systems can be easily applied to an LGAD-based silicon tracker at the EIC. We will address more details in future simulation work.

- *Elaborate on the collaboration with eRD24 and BNL.*

During the preparation of this proposal, we have already been in contact with the eRD24 team via regular discussions to explore synergies and establish a close collaboration between the two projects. There are a number of common issues and challenges we are going to address together. For example, one objective of this proposal is to explore much thinner (20–25  $\mu\text{m}$ ) LGAD sensors to achieve the optimal time resolution for the TOF application in a relatively low radiation environment like the EIC. The two project teams plan to fabricate such thin LGADs together utilizing the BNL facility. BNL has demonstrated the capability of fabricating LGADs with comparable performance to other world-leading vendors (HPK, FBK, CNM). In particular, BNL is one of only three sites in the world that are able to fabricate high quality AC-coupled LGADs. It is also the only LGAD producer in the US. Through this project, we aim to further strengthen the LGAD fabrication capability at BNL, specifically targeted for EIC applications. This effort also has the potential to bring down the detector construction cost in the longer term by establishing a domestic vendor.



One common challenge this and eRD24 project are facing is the strategy of developing the readout electronics, as pointed out by the committee. While it is unrealistic to solve the issue in this one-year project, we do have a viable path forward. Since the eRD24 project started one year earlier and has made significant progress in the AC-LGAD development, it would start shifting its focus toward ASIC readout chips, while this project will focus on the R&D of the new sensor design and characterization to meet the requirements of TOF and tracker. These coherent efforts will benefit each other and continue pushing the state-of-the-art LGAD technology for the EIC. The ORNL nuclear physics group on this proposal has longstanding experience in the development of readout electronic boards and chips for various experiments, most recently the ALICE barrel tracker. The group has a strong interest in applying their expertise to develop readout electronics for EIC detectors as a long-term commitment. More details can be found in Section 4.

## 2 Scientific Motivations

A high-energy high luminosity polarized electron-ion collider (EIC) will be constructed at Brookhaven National Laboratory (BNL) in the coming decade, as recently announced by the US Department of Energy. Its planned completion in 2030 will mark the start of the next new frontier of nuclear physics in which it will address many fundamental questions about the inner workings of a particle as ubiquitous as the proton. The EIC will, for the first time, precisely image gluons in nucleons and nuclei. It will definitively reveal the origin of the nucleon spin and will explore a new quantum chromodynamics (QCD) frontier of ultra-dense gluon fields, with the potential to discover a new form of gluon matter predicted to be common to all nuclei.

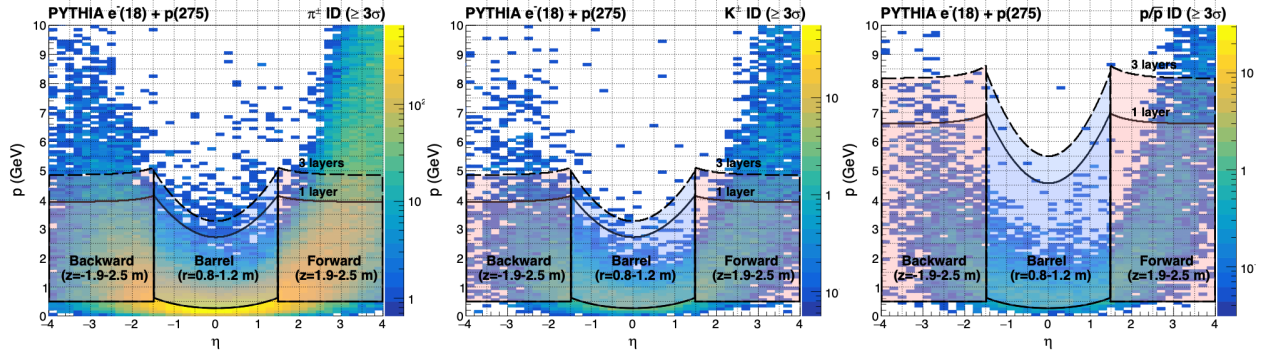
Furthermore, besides the “cold” QCD physics, discovery of novel collective effects in high-energy pp and pA collisions at RHIC and the LHC have raised the question if a hot and dense strongly-coupled quark-gluon liquid is formed there as in large AA collisions, and what is the smallest possible size of a hot QGP droplet [2, 3]. High-luminosity ep/eA collisions at EIC have unique advantages in providing an even smaller system with well-defined initial states and kinematics, and will thus open a new window to investigate “hot” QCD effects in small systems.

### 2.1 Particle identification at the EIC

Among many promising aspects of EIC physics [4], we are particularly interested in opportunities empowered by excellent particle identification capability over the full phase space. These topics include, but are not limited to:

- Flavor structure of the nucleon via exclusive and semi-inclusive DIS (SIDIS) measurements;
- Heavy flavor meson and baryon reconstruction (also inside jets);
- Search for long-range collective particle correlations in high-multiplicity events.

To extract 3-D flavor-dependent transverse momentum and spacial distributions of partons inside a nucleon, known as the TMD and GPD, separation of pions, kaons, and protons up to tens



**Figure 1:** Momentum and pseudorapidity distributions of pions, kaons and protons produced in electron (18 GeV) + proton (275 GeV) collisions from PYTHIA simulations. Shaded areas indicate PID coverage (more than  $3\sigma$ ) by a TOF-tracker system.

of GeV in the forward direction is crucial in covering the widest possible  $(x, Q^2)$  space. In the cases of heavy flavor hadron reconstruction and search for collectivity, low momentum PID with wide rapidity coverage is particularly important. The flight distance of very low momentum heavy flavor hadrons is too short for a secondary vertex to be resolved so PID provides the most important means to reject combinatorial backgrounds. Long-range collectivity observed in high-multiplicity hadronic collisions spans over at least 10 units in pseudorapidity, and is primarily a low- $p_T$  phenomenon (up to a few GeV). Measurements of long-range correlations with identified pions, kaons, and protons provide key discrimination power to different model scenarios (e.g., the mass order of PID spectra and anisotropic flow in hadronic collisions).

## 2.2 PID requirements at the EIC and the state-of-the-art technology

Two-dimensional momentum ( $p$ ) and pseudorapidity ( $\eta$ ) distributions of pions, kaons, and protons produced in simulated PYTHIA e(18 GeV)+p(275 GeV) collisions are shown in Fig. 1. Distributions of particles are highly asymmetric in  $\eta$  with more particles going in the forward direction, spanning well beyond 8–10 GeV in  $p$ . To fully meet the PID requirement at the EIC over extended phase space, complementary detector technologies are necessary. To identify very high momentum ( $p > 8$  GeV) hadrons primarily produced in the forward region ( $1.5 < \eta < 4$ ), the ring-imaging Cherenkov (RICH) detector is the only viable option. For PID in the intermediate and low momentum regions, several options have been proposed. A TOF system with a time resolution of about 20 ps will meet the requirement and provide a hermetic acceptance coverage. Technology based on the detection of internally reflected Cherenkov light (DIRC) is proposed to cover the midrapidity region ( $|\eta| < 1.5$ ), while modular Aerogel RICH can provide the coverage of forward and backward rapidity regions. Table 1(a) presents a summary of detector options in various regions of phase space. Their pros and cons have been extensively discussed (for a recent summary see e.g. [5]).

For the TOF system, there are several possible technologies proposed. The Large-Area Picosecond Photodetector (LAPPD) is the current best time resolution (5 ps) device known. The multigap resistive plate chamber (MRPC) has the advantage of being the most cost effective in cov-

(a) Phase space coverage of different PID technologies

	Backward ( $-4 < \eta < -1.5$ )	Central ( $ \eta  < 1.5$ )	Forward ( $1.5 < \eta < 4$ )
Low $p$ ( $< 3$ GeV)	TOF	TOF, TPC dE/dx	TOF
Intermediate $p$ (3-8 GeV)	TOF, RICH	TOF, DIRC	TOF, RICH
High $p$ (8-50 GeV)			RICH

(b) Complementarity of different TOF technologies

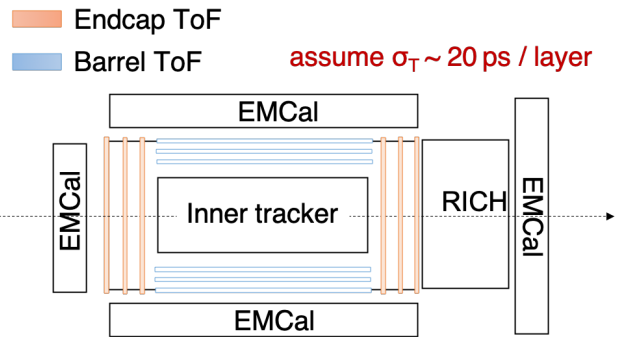
	LGADs	MRPC	LAPPD
Time resolution	20ps	20 ps	5ps
Spatial resolution	a few to hundreds $\mu\text{m}$	a few mm to 1 cm	1 mm
Overall thickness	2cm	10cm	2cm
High B field tolerant	Yes	Yes	No
Cost	High	Low	High

**Table 1:** (a) Phase space coverage of different PID technologies. (b) Complementarity of different TOF technologies.

ering large areas while also maintaining an excellent time resolution of about 20 ps. The LAPPD and MRPC can provide a hit position resolution of a few mm to 1 cm. In this proposal, we focus our interests on the low and intermediate momentum PID, and propose a hermetic TOF system based on a new silicon sensor technology, known as the low gain avalanche diodes (LGAD) [6–8]. The standard LGADs has a demonstrated time resolution of at least 30 ps, and are being used to construct a new TOF layer for the CMS and ATLAS experiments at the high-luminosity LHC. It sustains a high magnetic field environment (up to 4 T at CMS). Table 1(b) summarizes the proposed TOF technologies and their main specifications.

Besides an excellent time resolution, the LGADs also provide high-precision position resolution, and have the unique advantage of being highly compact, a feature that is crucial for an EIC detector with tight space constraints. A multi-layer LGADs system can serve as a dual-purpose detector, not only providing TOF measurements that meets the PID requirement at the EIC but also serving as (part of) a tracker for trajectory reconstruction and momentum determination. Compared to other proposed silicon tracking detectors, the Monolithic Active Pixel Sensors (MAPS) has the lowest material budget and very fine pixelization. It is the leading technology being considered for a high precision vertex detector. Although not the most competitive in low material budget, the precision timing of LGADs may (partially) compensate the larger multiple scattering effect in the track and vertex reconstruction. In the reconstruction of heavy flavor hadron decays, the PID capability provided by an LGAD-based outer tracker is particularly crucial in the very low momentum region.

A simplified design of a LGAD-based TOF-tracker system, consisting of three layers in the

**Figure 2:** A schematic design of a 3-layer TOF-tracker system based on LGADs.

barrel and endcap regions, is shown in Fig. 2. The three layers are placed at  $r = 0.8, 1.0, 1.2$  m for the barrel, and  $z = 1.9, 2.2, 2.5$  m for the endcap. Assuming a single-layer time resolution of 20 ps (which is the R&D goal as detailed later), the TOF-PID coverage is indicated in Fig. 1 as shaded areas for at least  $3\sigma$  separation. We consider two possible design scenarios: a single layer as a TOF (solid lines) or three layers as both a TOF and tracker (dashed lines). With a three-layer TOF-tracker system, pions and kaons identification can be achieved up to  $p \sim 5$  GeV, while proton identification can extend to  $p \sim 9$  GeV over  $|\eta| < 4$ . Lower limits in momentum reach are largely determined by the tracking capability, magnetic field, and material budgets. Here we assume a magnetic field of 1.5 T. Note that the design in Fig. 2 is only for demonstration purpose and can be further optimized to achieve better performance.

The main goal of this R&D proposal is to

- identify the optimal configuration (thickness and gain) of LGADs that is capable of providing the optimal time resolution of 20 ps or better to meet the TOF-PID requirements at the EIC;
- determine the requirement of position resolution as a tracker and investigate the feasibility of achieving high timing-position resolution with newly developed LGADs technologies.

### 3 Ultra-fast Silicon Sensor Technology

The Low Gain Avalanche Detector (LGAD) with internal gain [1, 6–10] is an ultra-fast silicon sensor technology, which has recently been chosen for constructing a fast-timing layer in the forward rapidity region of the CMS [11] and ATLAS [12] experiments at the high-luminosity (HL) LHC starting in 2028. The new timing layers will help the experiments mitigate significantly larger pileups of proton-proton interactions (up to about 200) foreseen at the HL-LHC by providing 4-D vertex reconstruction, and also serve as an excellent TOF system for hadron identification in QCD and heavy-ion physics.

In this section, we first introduce basics of the LGAD technology and its state-of-the-art performance in the application of the CMS endcap timing layer (ETL), a project most proponents of this proposal are deeply involved in. Next, we discuss the main limitations of current LGADs used by the LHC experiments, and lay out the proposed R&D path toward solving the issues and identifying a viable option for constructing a hermetic and compact TOF-tracker system at future EIC detectors.

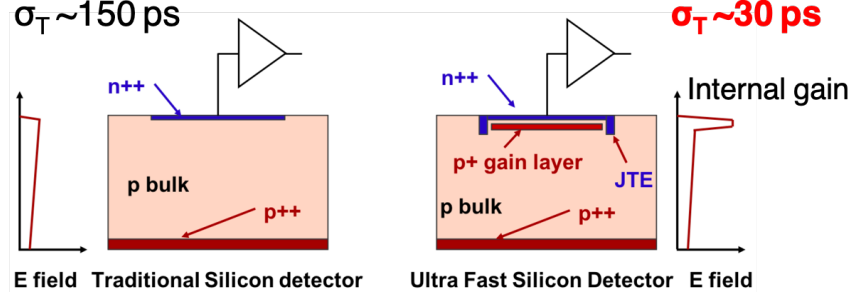
#### 3.1 The Low Gain Avalanche Detectors (LGAD)

The time resolution of silicon detector is largely dominated by the following terms:

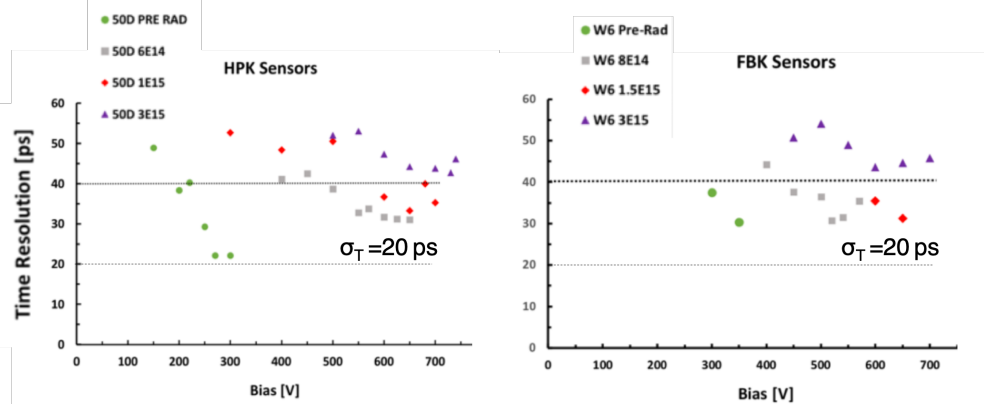
$$\sigma_t^2 = \sigma_{jitter}^2 + \sigma_{Landau}^2 + \sigma_{distortion}^2 + \sigma_{TDC}^2 \quad (1)$$

where,

- $\sigma_{jitter}^2$ : the jitter term is given by the ratio of the noise over the signal slew rate, where the noise is composed of electronic noise and sensor shot noise. The jitter is minimized by large signals, small capacitance, and fast rise time (inversely proportional to the sensor thickness).



**Figure 3:** Cross-sectional diagrams comparing a standard silicon detector and a LGAD (or UFSD) with an additional p implant providing the larger electric field needed for charge multiplication.



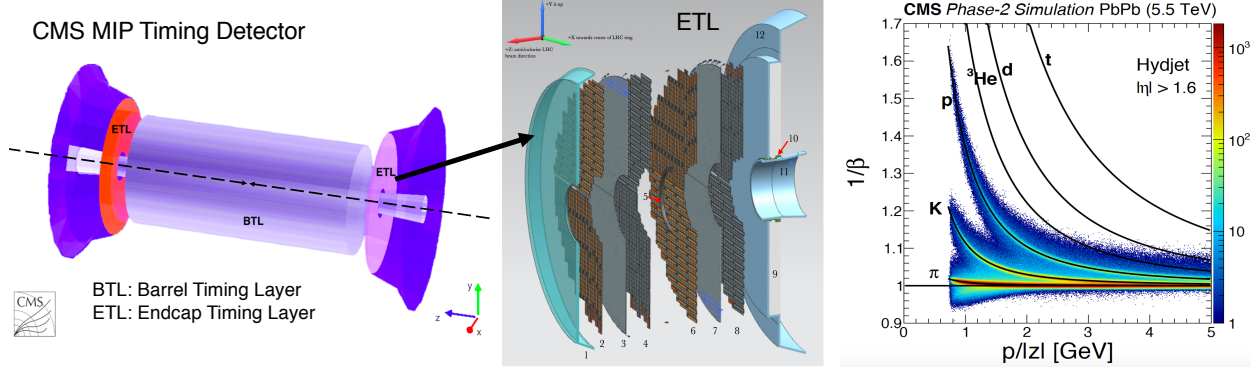
**Figure 4:** Time resolution measured as a function of bias voltage at different fluences for HPK and FBK LGAD sensors.

- $\sigma_{Landau}^2$ : the so-called Landau noise arises mainly from signal shape variations due to non-uniform ionization. This is the intrinsic limiting factor for the achievable time resolution. It can be improved by reducing sensor thickness.
- $\sigma_{distortion}^2$ : the distortion term is caused by the non-uniformity of weighting field and non-saturated drift velocity. This term can be largely suppressed by using a parallel plate geometry that has a uniform field and operating at a sufficiently high bias voltage to reach saturated drift velocity.
- $\sigma_{TDC}^2$ : the effect of the TDC binning.

Traditional  $n$ - $p$  silicon sensors with gains provided by external bias voltages, illustrated in Fig. 3 (left) can provide a typical time resolution on the order of 150 ps. The LGAD silicon sensors, shown in Fig. 3 (right), have an intrinsic gain of 10–30 provided by a special implant layer to generate a strong electric field locally and trigger avalanches. This internal gain helps the LGADs to achieve a low-jitter fast-rising pulse edge and overcome many other noise sources that enable high precision timing measurements for MIPs. LGAD sensors of 35–50  $\mu\text{m}$  in overall thickness can achieve a typical time resolution of about 30 ps.

Figure 4 shows the time resolution as a function of neutron and proton irradiation for HPK and FBK sensors [11]. These studies are consistent in demonstrating that ultra-fast silicon detectors





**Figure 5:** Left: a schematic view of CMS MTD. Middle: design of the two-disk ETL based on the LGADs. Right: TOF-PID performance of  $1/\beta$  vs.  $p$  in CMS Phase-2 simulations using HYDJET PbPb events.

(UFSDs) maintain a time resolution below 40 ps up to fluences of  $1.5 \times 10^{15}$  neq/cm<sup>2</sup> expected throughout the HL-LHC lifetime. Pre-radiation performance can even reach 20 ps, which is similar to condition foreseen at the EIC.

### 3.2 The CMS Timing Detector at the HL-LHC

As part of CMS Phase-2 upgrades planned at the HL-LHC, the MIP timing detector (MTD) will provide precision timing measurements with a resolution of 30–50 ps over a hermetic coverage of  $|\eta| < 3$ . The CMS MTD consists of a barrel (BTL) and an endcap (ETL) timing layer, covering pseudorapidity ranges of  $|\eta| < 1.5$  and  $1.6 < |\eta| < 3$ , respectively, to be installed between the outer tracker and electromagnetic calorimeter. Its cylindrical geometry has a radius of 1.16 m and a half-length of 3 m. A schematic diagram of the MTD is shown in Fig. 5 (left). LYSO crystal scintillators read out with silicon photomultipliers (SiPMs) have been chosen as a mature technology for the BTL, while LGAD silicon sensors have been identified as a viable implementation for the ETL. More details can be found in the Technical Design Report (TDR) [11]. Performance of the MTD as a TOF system for PID has been studied, led by proponents of this proposal, and is shown in Fig. 5 (right).

A cross-sectional view of the CMS ETL design on one side of the interaction region is shown in Fig. 5 (middle). The two-disk ETL system provides a measurement of two hits per track. Each disk has silicon devices on both faces to cover the whole area without cracks or gaps for readout electronics and services. Currently, three vendors are being evaluated as the potential provider of LGAD sensors, including HPK (Japan), FBK (Italy) and CNM (Spain).

Despite demonstrable, excellent performance at the level of a few channels, there are a few other challenges to overcome for constructing a large-scale TOF system consisting of more than 8 million of LGAD readout channels:

- ASIC readout chip is required to have a contribution to the overall time resolution of at most 30 ps, comparable to LGAD sensors, which have never been achieved before.
- The clock distribution system jitter should be no more than 10–15 ps, and negligible to the

overall time resolution.

- Radiation hardness of up to a total fluence of  $2 \times 10^{15} \text{ n}_{eq}/\text{cm}^2$  for the innermost radius. Bias voltages can be independently controlled as a function of radius to maintain the optimal gains over the entire ETL.
- Magnetic field tolerance of up to 4 T.
- Highly compact design with an allowed longitudinal space of 4.5 cm for two disks.

All these challenges are being addressed by ongoing R&D and prototyping efforts, which are expected to conclude by the engineering design review in mid-2022, followed by production, assembly, and final commissioning at the startup of HL-LHC. CMS heavy-ion physics groups (led by KU, MIT, RICE, and UIC) have a major involvement in the ETL project, particularly playing leading roles in the design and construction of service readout electronics and power systems (service hybrids), mechanical structure, as well as R&D, and testing of LGAD sensors and ASIC readout chips. During the production phase, RICE and KU groups will be responsible for QA/QC of service hybrids and LGAD sensors. The HL-LHC will provide the first testing ground of this new technology applied in a large collider experiment. This group aims to leverage the development and experience gained in the ETL project to bring a new and credible detector option to the EIC program.

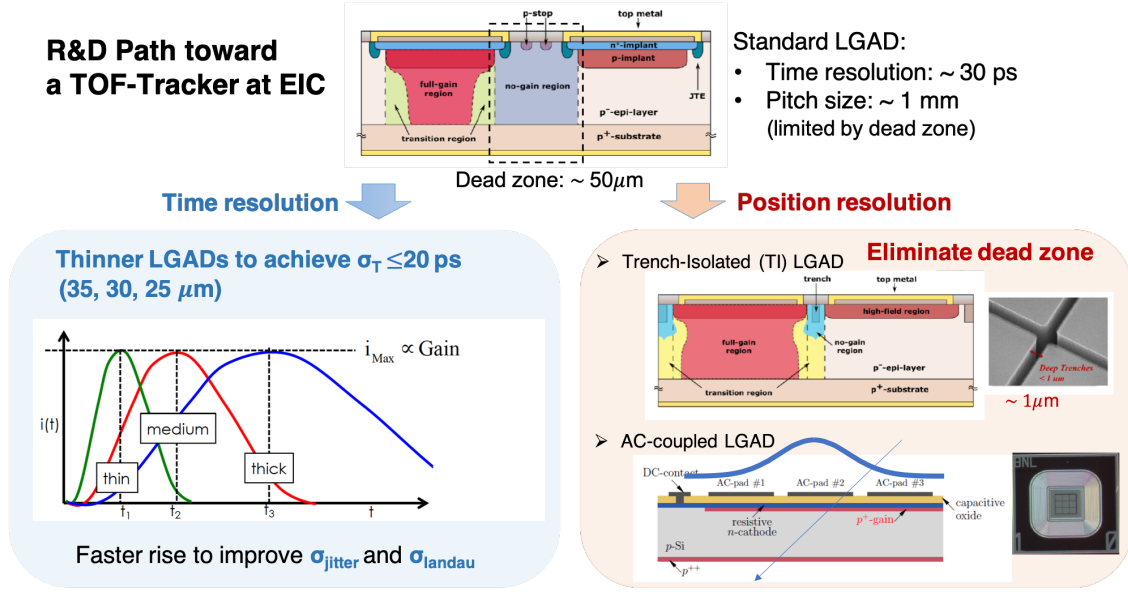
### 3.3 Limitations and R&D path toward a TOF-tracker system at the EIC

The LGADs provide an attractive option for constructing a compact, multi-layer system to simultaneously provide trajectory reconstruction and particle identification. One big advantage at the EIC is the much lower radiation level ( $\sim 10^{11} \text{ n}_{eq}/\text{cm}^2$ ) compared to the HL-LHC ( $\sim 2 \times 10^{15} \text{ n}_{eq}/\text{cm}^2$  for ETL). Therefore, radiation hardness of the detector is not a major concern. To fulfill the requirements for EIC physics, however, there are two limitations of the present LGADs that require improvements:

- Time resolution: while LGAD silicon sensors used by CMS and ATLAS can provide a time resolution of 30–50 ps, particle flight distance at EIC detectors is likely to be much shorter due to tight space constraints. Therefore, a time resolution of 20 ps or better is desired to meet the PID requirement at low momentum regions.
- Fill factor and position resolution: to serve as (part of) a tracker, a position resolution much better than the 1 mm pixel size has to be accomplished to be competitive to other types of silicon pixel and/or strip sensors that are designated for position measurements. The current limitation lies in the approximately 50  $\mu\text{m}$  width of the intra-pad no-gain region, which is needed to protect against early breakdowns. Smaller pixel sizes would lead to too low fill factors, or loss of acceptance. The CMS ETL has a fill factor of 85% per disk, with the two-disk system compensating for a 100% acceptance.

Owing to rapid developments of the LGAD technology in recent years, potential solutions to limitations mentioned above already exist. An R&D program targeted specifically on meeting





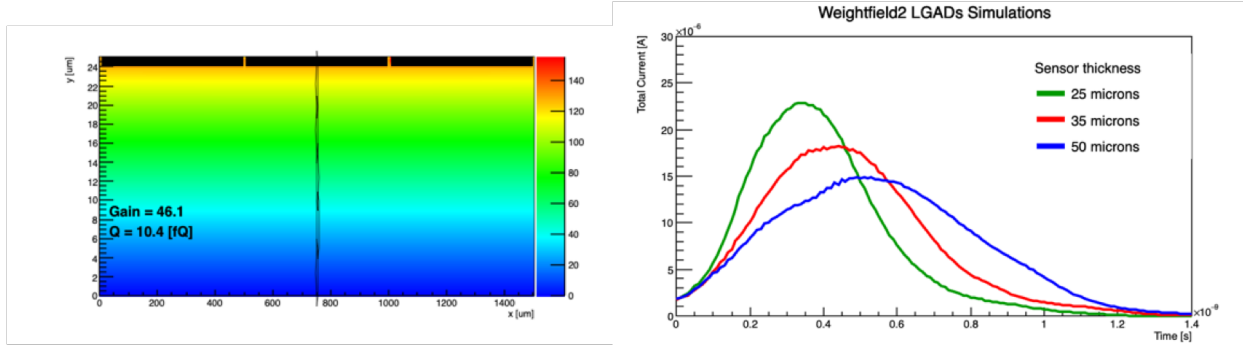
**Figure 6:** Summary of the R&D path toward realizing a TOF-tracker at EIC using LGADs.

requirements of EIC detectors will address both limitations, as detailed below and illustrated in Fig. 6:

- The jitter contribution to the time resolution is directly related to the signal slew rate, which is inversely proportional to the sensor thickness (illustrated in Fig. 6, left). Reducing the thickness from  $50 \mu\text{m}$  to 35, 30, and even  $25 \mu\text{m}$  will not only improve the jitter but can also suppress the Landau noise. Note that the total charge collection will also be reduced proportionally, leading to a smaller signal. Gain layers with a higher doping concentration may be needed to compensate for it.

Thinner LGADs are not applicable for the HL-LHC due to faster degradation of performance (i.e., sufficiently large gains are difficult to maintain) after irradiation. As radiation hardness is not a main concern at EIC, we can focus the R&D on identifying the optimal thickness and gain scheme of LGADs that provide the best time resolution.

- To achieve better position resolution (beyond 1 mm pixel size), two viable solutions are present, illustrated in Fig. 6 (right). Trench-isolated (TI) LGADs is capable of reducing the no-gain region down to a width of only a few  $\mu\text{m}$ , essentially eliminating it to achieve 100% fill factor. All readout schemes can be kept the same as standard LGADs. For AC-coupled LGADs, segmentation is not done on the silicon sensor but at metallic readout contacts sitting on top of a dielectric layer, reading out induced charges. The fill factor is effectively 100%. The signal pulse is shared among several adjacent pads, further improving its position sensitivity. The metallic readout pads can be fabricated into pixels, strips or any shape desired. BNL is among leading developers of AC-LGADs (others include FBK and CNM).



**Figure 7:** Weightfield2 LGADs simulations: example of potentials and gain in a 25  $\mu\text{m}$  sensor (left) and comparison of currents from 25, 35, 50  $\mu\text{m}$  sensors with a total charge collection of about 10 fC (right).

## 4 Proposed Work and Deliverables

In this one-year R&D program, we will address the most pressing issues as outlined in Section 3.3 and Fig. 6. In this section, we discuss our proposed work in three parts, including deliverables and a timeline. We also provide prospects on future developments of the readout electronics and mechanical design, although it is unrealistic to fit them into the scope of this one-year program.

The team is highly experienced in fast-timing silicon technology. Besides the MTD and CT-PPS projects at the CMS experiment, Prof. C. Royon is also leading KU's side of the NASA AGILE project to construct a cosmic-ray telescope detector in space based on the LGADs. Full teststands with infrared-laser based TCT and  $\beta$ -sources are available at RICE and KU, where the work on sensor characterization will be primarily carried out. ORNL has a strong nuclear physics group on the ALICE and (s)PHENIX experiments, with strong expertise in developing readout chips and boards. Additionally, the RICE group has lead the R&D, design, and construction of STAR's TOF systems, including its electronics. It is worth mentioning that we have been generously offered help from our CMS collaborators at INFN Torino and FNAL which includes technical support and consultants, use of testing facilities, and the procurement of specific LGAD sensors as needed. Opportunities of participation in beam tests at FNAL may become available as part of the CMS ETL program.

Meanwhile, because of the technical complementarity, we have established a close collaboration with the eRD24 team led by E.C. Aschenauer and A. Tricoli at BNL, who are developing AC-LGADs for far-forward Roman Pot stations. We will be tackling a number of common issues and challenges together, and share resources (including testing equipment, BNL silicon fabrication facility, etc.) to maximize the outputs of both projects. Details of collaboration with eRD24 are elaborated later.

### 4.1 Part I: R&D of ultra-thin LGAD sensors (10/2020–03/2021)

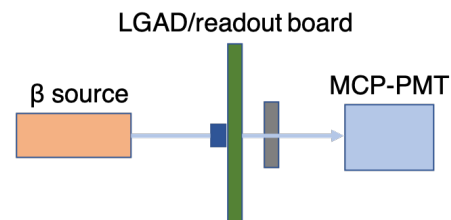
Our first step will focus on establishing the best possible time resolution (20 ps or better) via a systematic characterization of the thinner LGAD sensors. This part of the work will mainly be

carried out by a postdoc at RICE (S. Yang) and an undergraduate researcher at KU.

We have performed simulations with WeightField2 to predict signal currents of LGADs with thicknesses of 50, 35, and 25  $\mu\text{m}$ , as shown in Figure 7. For each thickness option, the bias voltage is chosen to provide a gain that generates a total charge collection of about 10 fC, which is the minimum amount of charge required for a quality timing measurement. Signals of thinner sensors have faster slew rates and a higher peak values for fixed total charge collection, which is expected to provide a better timing performance.

We will carry out measurements to verify the simulation results in Fig. 7. Sample sensors of 50  $\mu\text{m}$  (chosen by CMS and ATLAS) and 35  $\mu\text{m}$  in thickness are available from HPK, FBK, and CNM. Standard LGADs with 55, 45, and 25  $\mu\text{m}$  thickness will also be available from FBK soon. Our CMS collaborator, Nicolo Cartiglia (INFN Torino), will kindly help us acquire a set of sample sensors with desired configurations. Furthermore, we will work with the eRD24 team to attempt the production of such thinner LGADs at BNL and verify their performance. The eRD24 team includes experts from the BNL instrumentation department, who have demonstrated the capability of fabricating LGADs with comparable performance to other world-leading vendors (HPK, FBK, CNM). In particular, BNL is one of only three sites in the world that are able to fabricate the novel AC-coupled LGADs. It is also the only LGAD producer in the US. Strengthening the LGAD fabrication capability at BNL through this project, specifically targeted on the EIC application, will help establish a quality domestic vendor and bring down the detector construction cost in the longer term.

Sensor characteristics and performance (e.g. gains, time resolution) for each thickness option will be studied and compared using infrared laser TCT and  $\beta$  sources. An MCP-PMT (e.g., from HPK or PHOTEK) will be used as a trigger reference detector with a time resolution of 10–15 ps. Signals will be extracted by mounting sample sensors onto a UC Santa Cruz readout board for further analyses. We also plan to test sensor performance under different temperature conditions. A schematic of our test stand is shown in Fig. 8, which is a typical setup for characterizing basic LGAD performance in the CMS ETL program. If opportunities for beam tests at FNAL become available (for CMS ETL, possibly early next year), we will seek to join the program so that more detailed characterization of thinner LGADs can be obtained using high-energy particles.



**Figure 8:** A schematic of teststand for LGADs characterization.

Deliverables of Part I from 10/2020 to 03/2021 are summarized below:

- Detailed characterization of standard LGADs for a variety of sensor thicknesses using lasers,  $\beta$  sources, and (if possible) proton beams at FNAL to determine the LGADs configuration optimized for the best time resolution without irradiation.
- Fabrication of thin standard LGADs at BNL in collaboration with the eRD24 team.

## 4.2 Part II: simulations of a LGAD-based TOF-tracker (10/2020–05/2021)

In parallel with work of optimizing LGADs timing resolution in Part I, we will proceed with simulations studies to fully evaluate the required position resolution of LGADs for a tracker. This includes evaluating the impact of material budgets, its optimal location and layout, and benefits of precision timing information to track momentum and back-pointing resolutions. This part of the work has a strong synergy with the EIC tracker/tracking group, which we will capitalize on to strengthen the collaboration and avoid duplication of work.

Proposed simulation work will be led by a postdoc at ORNL and one graduate student from RICE. We will carry out the work within the simulation framework established by the EIC software consortium and implement the LGADs layers with a layout sketched in Fig. 2: first in fast simulations with `eic-smear` library, and then in full simulations with `Fun4All`. We will primarily investigate the option of placing LGADs layers in the outermost region of the tracker to benefit from long flight distances for PID. For the inner tracker, we will make use of existing implementations such as MAPS-based silicon pixel detectors.

Deliverables of Part II from 10/2020 to 05/2021 are summarized below:

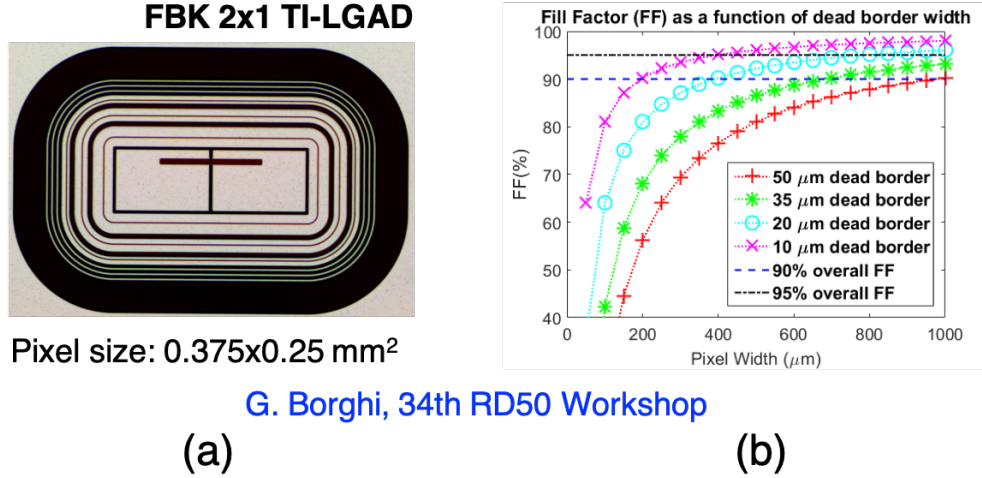
- Realistic TOF-PID performance, including effects of start-time ( $t_0$ ) determination, based on sensor time resolution achievable from work in Part I.
- Determination of LGADs pitch sizes needed to achieve the required track momentum and back-pointing resolutions at EIC.
- Studies of benefits from combined position and timing information to tracking.

## 4.3 Part III: R&D of AC-coupled and TI-LGAD sensors (03/2021–09/2021)

With work in Part I and II accomplished, we arrive at the final step of this project to realize LGAD sensors with both high timing and position resolution for a TOF-tracker system at the EIC. This part of the work will be carried out by the same workforce mentioned in Part I. As introduced in Section 3.3, LGADs currently used by the CMS and ATLAS experiments have a pitch size of  $\sim 1$  mm, limited by the intra-pad no-gain region. This is likely insufficient precision for a tracker but possible solutions already exist.

The LGADs with trench isolation (TI-LGAD) have been fabricated by FBK for  $50\text{ }\mu\text{m}$  thickness [13]. Figure 9 (left) shows the image of a sample TI-LGADs with  $2 \times 1$  pixels and a pixel size of  $375 \times 250\text{ }\mu\text{m}^2$ . The no-gain region is reduced to the order of a few microns. Figure 9 shows how the fill factor depends on the pixel width and size of non-gain region. With the TI-LGADs, a fill factor of more than 90% for a pixel width of  $100\text{--}200\text{ }\mu\text{m}$  is within the reach.

With our setups in Part I, we will test sample TI-LGADs sensors and perform a direct comparison with standard LGADs sensors with the same thickness. If performance in terms of time resolution is comparable, we will seek for the possibility of producing TI-LGADs by FBK with thickness determined from Part I and pitch size determined from simulations in Part II. We should note that new sensor production may be time-consuming and require coordination with vendor's schedule so we cannot fully guarantee this to be done within this one-year project. Nevertheless,



**Figure 9:** (a) a  $2 \times 1$  TI-LGAD sensor fabricated by FBK; (b) simulations of fill factors as a function of pixel width for different width of no-gain region [13].

it is promising to pursue this possibility even for a little longer term. It will be a vital contribution to the development of LGADs technology in general.

Another solution to reach high precision in position is the AC-coupled LGADs. As mentioned earlier, BNL is a world-leading developer in AC-LGAD technology. Sensors developed and fabricated by the BNL group are shown in Fig. 10 (left), including both pixel and strip configurations with a smallest pitch size of  $\sim 200 \mu\text{m}$  [14]. The AC-LGADs of  $55 \mu\text{m}$  thick are also fabricated by FBK. Figure 10 (right) shows recent testing results of FBK AC-LGADs sensors using infrared lasers [15]. A time resolution of about 22 ps is achieved. More impressively, a position resolution of a few  $\mu\text{m}$  is achieved with a pitch size of  $200 \mu\text{m}$ , thanks to the signal sharing among multiple readout pads.

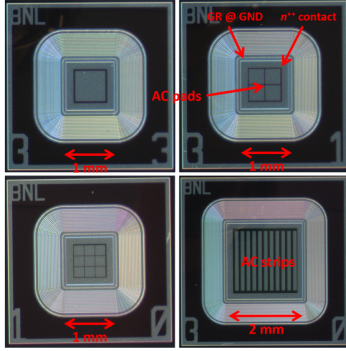
Currently, the BNL group is developing the AC-LGAD technology for constructing high position and timing resolution Roman Pots under the BNL EIC R&D program. We will establish a strong collaboration to leverage the synergy between the two projects. In particular, the BNL instrumentation group has the capability of fabricating AC-coupled LGADs with specifications (thickness, granularity) determined from this proposal for a TOF-tracker system.

Deliverables of Part III from 03/2021 to 09/2021 are summarized below:

- Studies of the performance of the FBK TI-LGAD and direct comparisons to standard LGAD at the same thickness ( $50 \mu\text{m}$ ). If comparable performance in time resolution without irradiation is demonstrated, seek for production of thinner TI-LGADs (if possible and time allows) with desired pixel sizes determined by simulations in Part II.
- Fabrication of AC-coupled LGADs with desired thickness and pixel sizes at BNL in collaboration with the eRD24 team.



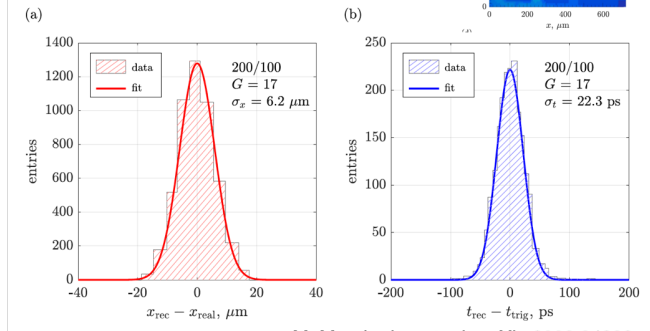
#### BNL AC-LGAD



G. Giacomini et. al., JINST 14 (2019) 09, P09004

(a)

#### Laser-test results of FBK AC-LGAD



M. Mandurrino et. al., arXiv:2003.04838

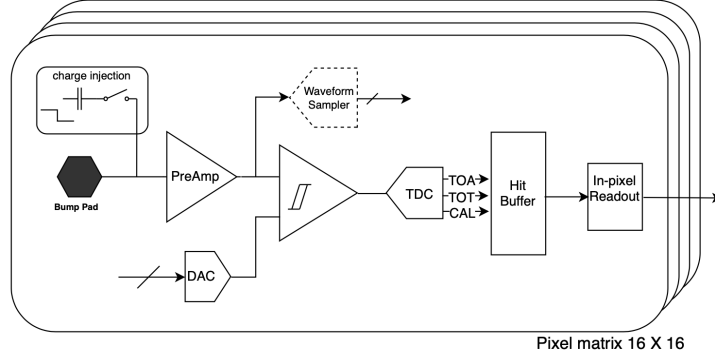
(b)

**Figure 10:** (a) AC-LGADs in pixels and strips fabricated by BNL [14]; (b) test results of FBK AC-LGADs by lasers on timing and position resolutions [15].

## 4.4 Prospects on readout electronics and mechanical design

In the actual detector system, the LGAD's signals will be extracted and digitized by ASIC readout chips, which should have timing capabilities that match the sensors. Each ASIC readout channel typically consists of a pre-amplifier, a discriminator, and a time-to-digital converter (TDC) to digitize the TOA (time of arrival) and TOT (time over threshold) measurements, as well as a memory unit for data storage and readout. A schematic of the CMS ETL ASIC (ETROC) design can be seen in Fig. 11. ASICs and LGADs will be bump-bonded together so their area ratio is 1:1. The ultimate challenge for the signal readout design is that all circuit components must fit within the size of a single LGAD pixel, which in turn, is driven by the requirement of specific measurements. The ASICs currently being designed for the CMS and ATLAS timing detectors aim for a pixel size of  $1.3 \times 1.3 \text{ mm}^2$ . A pixelization of  $0.5 \times 0.5 \text{ mm}^2$  should be achievable with reasonable efforts. Any finer pixelization, if required (likely the case at inner radii of a tracker), would require significant efforts in re-designing the ASICs. It is worth mentioning that the cost of ASICs is mainly driven by the total area, instead of total number of channels, so the cost will not drastically increase with finer pixelization.

We plan to address the ASIC readout challenge together with the eRD24 team in strategic steps. Since the eRD24 project started one year earlier and has made significant progress in the AC-LGAD development, we decided that eRD24 will start shifting its focus toward the ASIC design, while this one-year project still focuses on the R&D efforts of pushing the state-of-the-art LGAD technology to meet the TOF and tracker requirements at the EIC. The ASIC experts from Omega and IJCLab (FR), who have developed ALTIROC for the ATLAS timing detector, have joined the eRD24 project. The goal in the next year is to realize an ASIC pixel of  $0.5 \times 0.5 \text{ mm}^2$  as a first step, which meets the needs for the Roman Pot stations. Going further for a tracker, the outcome of this project will determine more quantitatively the needs so that a longer-term strategy of ASIC designs can be laid out. For example, efforts on re-designing the TDC units to fit into a smaller area



**Figure 11:** A schematic of the CMS ETL ASIC, showing the blocks within each channel of the  $16 \times 16$  pixel matrix [11].

is needed with most advanced CMOS technology (130 nm, 65 nm, 28 nm). The ORNL nuclear physics group from this proposal has a long tradition in developing readout electronic boards and chips for various experiments, most recently the ALICE barrel tracker. The group has a strong interest in applying their expertise to develop readout electronics for EIC detectors as a long-term commitment.

In terms of mechanical designs, note that our proposal is primarily to apply LGADs to outer layers of a tracker to benefit longest flight distance for PID, not an entire tracker based on the LGADs. The layout of the two-disk CMS ETL shown in Fig. 5 can be essentially considered a proto-design of outer layers of an endcap tracker. Furthermore, both CMS and ATLAS will adopt an all-silicon tracker for the HL-LHC program, coverage  $|\eta| < 4$ . Mechanical designs of those systems should be easily applied to an LGAD-based silicon tracker at the EIC. We will address more details in future simulation work.

## 5 Proposed Budget and Labor

### 5.1 Budget justification

#### Labor

We propose 0.6 FTE (\$54k) support of a current RICE postdoc, Shuai Yang, and partial support (\$3k) of an undergraduate researcher at KU, William Doumerg, by the EIC detector R&D program and lead most of the proposed detector work defined in Parts I and III. RICE postdoc, Shuai Yang, is an expert on photonuclear interactions and a key member of the RICE team on ETL. He has performed simulations for heavy-ion physics cases and is working on testings of ETL prototype readout boards.



## M&S

We will leverage as much as possible existing resources, lab facilities and personnel at each institute, especially those from the CMS ETL and NASA AGILE projects so only a small amount of M&S funds (\$5k) is requested mainly for acquiring and producing LGAD sensors. We will also work closely with eRD24 to optimize the usage of our M&S sources for the LGAD fabrication.

## Travel

Given the uncertainty of COVID-19 pandemics, we request very limited travel funds (\$1k) for ORNL to make one domestic trip to RICE and collaborate on the simulation work for a period of time. We will use external resources for additional travels if allowed and needed.

## 5.2 Budget summary including reduced scenarios

Budget breakdowns by costed items and by institutions are summarized in Tables 2 and 3, respectively.

As per the guideline, we also include scenarios of reduced total budget by 20% and 40%, respectively.

- In 20% reduced scenario, we will drop any new fabrication of LGADs, or seek for other resources. With reduced postdoc FTEs, we will focus on studying only AC-coupled LGADs (dropping TI-LGAD) in collaboration with the BNL group for Part III.
- In 40% reduced scenario, it would only be possible to pursue the Part I of this proposal to focus on optimizing the timing performance.

**Table 2:** Budget breakdown by costed items, including reduced scenarios by 20% and 40%.

Costed Item	100%	80%	60%
Labor	\$57k	\$48k	\$36k
M&S	\$5k	\$2k	\$2k
Travel	\$1k	\$1k	0
Total	\$63k	\$51k	\$38k

**Table 3:** Budget breakdown by institutions, including reduced scenarios by 20% and 40%.

Institute	100%	80%	60%
RICE	\$59k	\$47k	\$38k
KU	\$3k	\$3k	0
ORNL	\$1k	\$1k	0
Total	\$63k	\$51k	\$38k

### **5.3 Contributed resources**

One graduate student from RICE and 0.5 FTE of one postdoc from ORNL (a new position is being filled on sPHENIX and EIC) will lead the simulation work and assist in the proposed detector activities as contributed resources. At KU, Alexander Novikov (post-doc), Tommaso Isidori, and Florian Gautier (graduate student), who are working on the NASA project, can provide assistance to this project.

## References

- [1] A. Apresyan et al., “Studies of Uniformity of 50  $\mu\text{m}$  Low-Gain Avalanche Detectors at the Fermilab Test Beam”, *Nucl. Instrum. Meth. A* 895 (2018), 158.
- [2] K. Dusling, W. Li, and B. Schenke, “Novel collective phenomena in high-energy proton-proton and proton-nucleus collisions”, *Int. J. Mod. Phys. E* 25 (2016), 1630002, arXiv: [1509.07939 \[nucl-ex\]](#).
- [3] J. L. Nagle and W. A. Zajc, “Small System Collectivity in Relativistic Hadronic and Nuclear Collisions”, *Ann. Rev. Nucl. Part. Sci.* 68 (2018), 211, arXiv: [1801.03477 \[nucl-ex\]](#).
- [4] A. Accardi et al., “Electron Ion Collider: The Next QCD Frontier: Understanding the glue that binds us all”, *Eur. Phys. J. A* 52 (2016), ed. by A. Deshpande, Z. Meziani, and J. Qiu, 268, arXiv: [1212.1701 \[nucl-ex\]](#).
- [5] P. Rossi et al., *PID summary*, Talk at 2nd EIC Yellow Report Workshop at Pavia University, <https://indico.bnl.gov/event/8231/contributions/37909/attachments/28331> May 2020.
- [6] S. N. White, “R&D for a Dedicated Fast Timing Layer in the CMS Endcap Upgrade”, *Acta Phys. Pol. B Proc. Suppl.* 7 (2014), 743, arXiv: [1409.1165 \[physics.ins-det\]](#).
- [7] G. Pellegrini et al., “Technology developments and first measurements of Low Gain Avalanche Detectors (LGAD) for high energy physics applications”, *Nucl. Instrum. Meth. A* 765 (2014), 12, ISSN: 0168-9002.
- [8] N. Cartiglia et al., “Design optimization of ultra-fast silicon detectors”, *Nucl. Instrum. Meth. A* 796 (2015), 141.
- [9] D. Breton et al., “Measurements of timing resolution of ultra-fast silicon detectors with the SAMPIC waveform digitizer”, *Nucl. Instrum. Meth. A* 835 (2016), 51, arXiv: [1604.02385 \[physics.ins-det\]](#).
- [10] N. Minafra et al., “Test of Ultra Fast Silicon Detectors for Picosecond Time Measurements with a New Multipurpose Read-Out Board”, *Nucl. Instrum. Meth. A* 867 (2017), 88, arXiv: [1704.05298 \[physics.ins-det\]](#).
- [11] C. Collaboration, *A MIP Timing Detector for the CMS Phase-2 Upgrade*, tech. rep. CERN-LHCC-2019-003. CMS-TDR-020, Geneva: CERN, Mar. 2019, URL: <https://cds.cern.ch/record/>
- [12] A. Collaboration, *Technical Proposal: A High-Granularity Timing Detector for the ATLAS Phase-II Upgrade*, tech. rep. CERN-LHCC-2018-023. LHCC-P-012, Geneva: CERN, June 2018, URL: <https://cds.cern.ch/record/2623663>.
- [13] G. Borghi et al., *Development and First Parametric Characterization of High Fill-Factor Segmented LGADs with Trench Isolation*, Talk at 34th RD50 Workshop, June 2019.
- [14] G. D. G. Giacomini W. Chen and A. Tricoli, “Fabrication and performance of AC-coupled LGADs”, *JINST* 14 (2019), P09004, arXiv: [1906.11542](#).
- [15] M. Mandurrino et al., “High performance picosecond- and micron-level 4D particle tracking with 100% fill-factor Resistive AC-Coupled Silicon Detectors (RSD)” (Mar. 2020), arXiv: [2003.04838 \[physics.ins-det\]](#).